

Routing Algorithms In Networks-on-Chip

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Designing Robust Routing Algorithms and Mapping Cores in Networks-on-Chip: A Multi-objective Evolutionary-based Approach. Maurizio Palesi (Kore University, Italy)

A case for bufferless routing in on-chip networks

A Case for Bufferless Routing in On-Chip Networks Thomas Moscibroda Microsoft Research load-balanced adaptive routing algorithm for torus networks. In ISCA, 2003.

Simulation and analysis of multicast routing

Research Article: Simulation and Analysis of Multicast Routing Algorithm for 2-D Mesh Network on Chip

Low-overhead routing algorithm for 3d network-

Low-overhead Routing Algorithm for 3D Network-on-Chip Akram Ben Ahmed, Abderazek Ben Abdallah The University of Aizu Graduate School of Computers Science and Engineering

Network on chip routing algorithms - techylib

document titled Network on Chip Routing Algorithms is about the most essential things are a network topology and a routing algorithm. Routers route the packets

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viii Preface a brief introduction on the basic concepts of on-chip networks, routing algorithms for NoC architectures will be presented and discussed at different

Fault-aware and reconfigurable routing algorithms

IETE JOURNAL OF RESEARCH | VOL 57 | ISSUE 3 | MAY-JUN 2011 215 Fault-aware and Reconfigurable Routing Algorithms for Networks-on-Chip Mojtaba Valinataj, Siamak

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BibTeX @MISC{Rantala06networkon, author = {Ville Rantala and Teijo Lehtonen and Juha Plosila}, title = {Network on Chip Routing Algorithms}, year = {2006}}

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Routing Algorithms in Networks-on-Chip. Editors: Maurizio Palesi, Learning-Based Routing Algorithms for On-Chip Networks. Masoumeh Ebrahimi, Masoud Daneshtalab.

Network on a chip - wikipedia, the free

Network on chip or network the algorithms must be designed in such a way that they offer large parallelism The routing algorithms should be implemented

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Genre/Form: Electronic books: Additional Physical Format: Print version: Palesi, Maurizio. Routing Algorithms in Networks-on-Chip. Dordrecht : Springer, 2013

Cluster based hierarchical routing algorithm for

U. Saravanakumar, R. Rangarajan, R. Haripriya, R. Nithya and K. Rajasekar, "Cluster Based Hierarchical Routing Algorithm for Network on Chip," Circuits and Systems

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Inbunden, 2013. Pris 1322 kr. K p Routing Algorithms in Networks-on-Chip (9781461482734) av Maurizio Palesi, Masoud Daneshtalab p Bokus.com

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Fuzzy-based adaptive routing algorithm for

MAFA: adaptive fault-tolerant routing algorithm for networks-on-chip, in: Proceeding of 15th Euromicro Conference on Digital System Design (DSD),

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Application specific routing algorithms for

In this paper, we present a methodology to design highly adaptive routing algorithms for NoC architectures that do not require the use of any VCs.

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Chapter 5 Routing algorithms for workload consolidation Sophisticated routing algorithms can improve the network for adaptive routing in networks-on-chip.

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Ville Rantala | Teijo Lehtonen | Juha Plosila Network on Chip Routing Algorithms TUCS Technical Report No 779, August 2006 Network on Chip Routing Algorithms Ville

Deadlock free routing algorithm for minimizing

Deadlock Free Routing Algorithm for Minimizing Data Packet Transmission in Network on Chip: 10.4018/jertcs.2012010104: Network on chip (NoC) has been proposed as a

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